DSD 2019
22nd Euromicro Conference on Digital System Design
Kallithea, Chalkidiki, Greece, Aug. 28th – Aug. 30th, 2019

SPECIAL SESSION on “Architecture and Hardware for Security Applications (AHSA)”

SPECIAL SESSION SCOPE
The scope of this special session is on all views of hardware-oriented security. The session provides an excellent opportunity to present new results from both the research and industry communities. Of special interests are contributions that describe new methods for secure-and-efficient hardware implementations for embedded systems, hardware Trojans – insertion/detection methodologies, side channel analysis, fault attacks, security of Internet of Things, security of IoT etc. The topics of this special session include but are not limited to:

- Applications of secure hardware
- Attacks against hardware implementations and countermeasures
- Hardware Trojans (attacks, detection, countermeasures)
- IC Trust and anti-Counterfeiting
- Reverse engineering and hardware obfuscation
- Hardware security primitives: PUFs and TRNGs
- Security of Cyber-Physical Systems
- Security architectures in Embedded Systems
- Protection of Industrial Internet of Things (IIoT)
- Protection of Internet of Things (IoT)
- Crypto implementations in area constrained environments
- Processors for smart card security
- Machine learning for hardware security
- Energy-aware secure architectures
- Security architectures for pervasive computing and wireless applications/protocols
- Trusted computing platforms
- Secure System-on-Chip (SoC) designs
- Lightweight cryptography and implementations
- Vehicular security hardware implementations
- Hardware architectures for cryptanalysis
- Reconfigurable cryptographic primitives
- Performance evaluation of secure hardware

Submission Guidelines
Authors are encouraged to submit their manuscripts via Easychair web service at web page https://easychair.org/conferences/?conf=dsd2019. Should an unexpected web access problem be encountered, please contact the Program Chair by email (dsd2019@easychair.org, nkonofaos@csd.auth.gr or pkitos@teimes.gr). Each manuscript should include the complete paper text, all illustrations, and references. The manuscript should conform to the IEEE format: single-spaced, double column, US letter page size, 10-point size Times Roman font, up to 8 pages. In order to conduct a blind review, no indication of the authors’ names should appear in the manuscript, references included.

CP5, Conference Publishing Services, publishes the (ISI indexed) DSD Proceedings, available worldwide through the IEEE Xplore Digital Library. Extended versions of selected best papers will be published in a special issue of the ISI indexed “Microprocessors and Microsystems: Embedded Hardware Design” Elsevier journal.

IMPORTANT DATES
Deadline for paper submission: April 21st (extended)
Notification of acceptance: May 31st
Camera ready papers: June 23rd

MORE INFORMATION (WEB PAGES)
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